

## PATENT ABSTRACTS OF JAPAN

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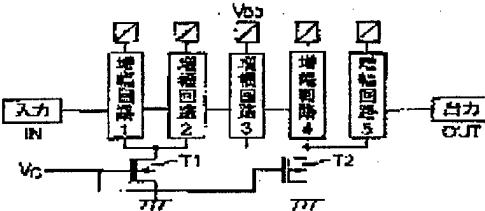
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### (54) AMPLIFIER CIRCUIT

#### (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide such amplifier circuits adopting a configuration of a plurality of stages having off-leak current interruption transistors (TRs) that input sensitivity is enhanced by noise prevention and a signal amplification characteristic is improved by suppressing a deviation in a circuit threshold value among the amplifier circuits.

**SOLUTION:** The amplifier circuits of the invention are connected between a signal input node and a signal output node in cascade and receive a power supply level at respective terminals, provided with output circuits, with a 1st switching element that is connected between the other respective terminals of the amplifier circuits of a plurality of the stages connected in common and a ground node and has a threshold value higher than switching elements being components of the amplifier circuits, and with a 2nd switching element that is connected between the other respective terminals of the output circuits and a ground node and has a threshold value higher than switching element being components of the output circuits.



### LEGAL STATUS

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